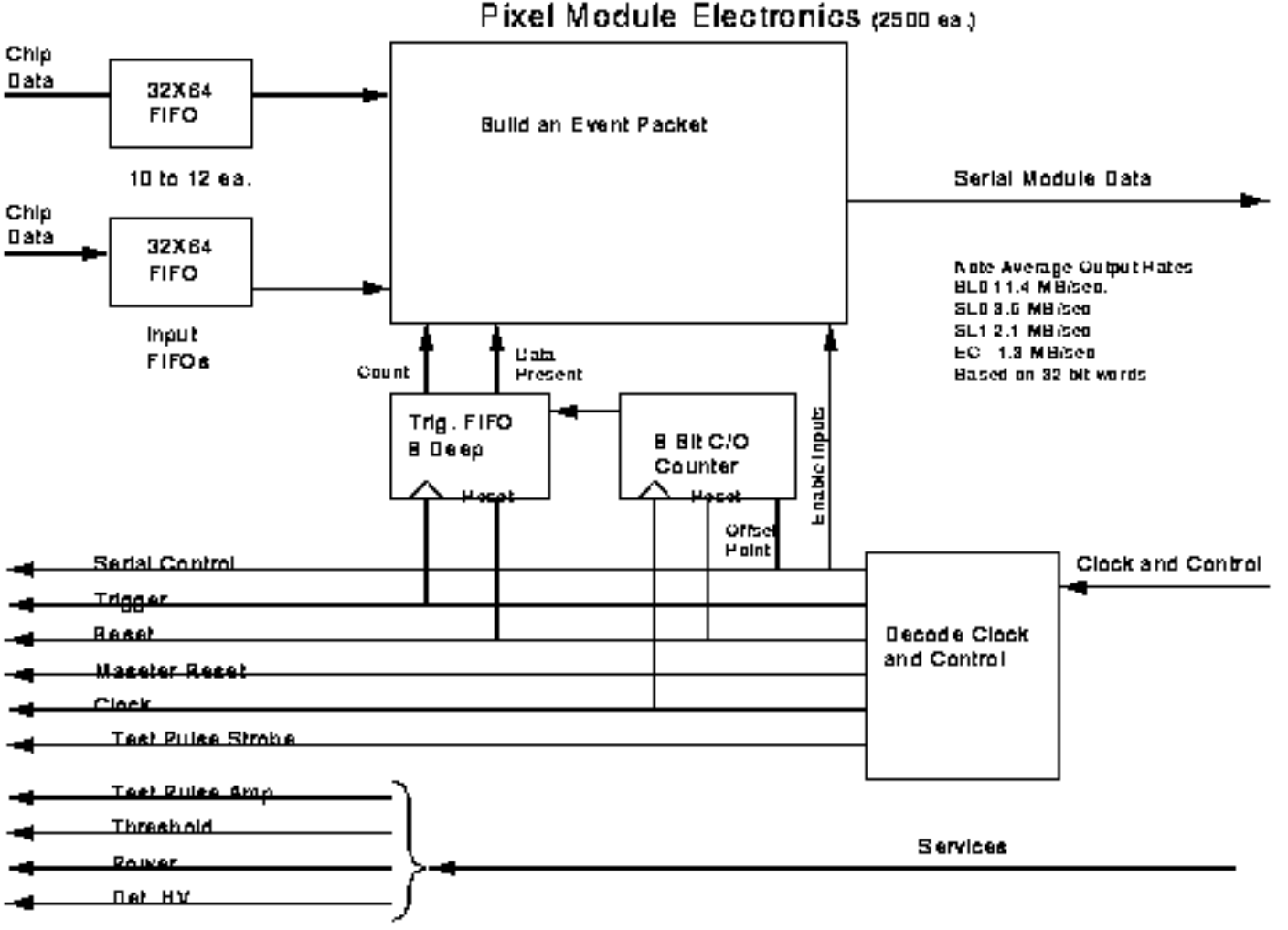
Pixel Module Electronics (2500 ea.)
Chip Data
32X64 FIFO
10 to 12 ea.
Chip Data
32X64 FIFO
Input FIFOs
Build an Event Packet
Serial Module Data
Note Average Output Rates
BL0 11.4 MBits/ea.
SL0 3.6 MBits/ea
SL1 2.1 MBits/ea
EC 1.3 MBits/ea
Based on 32 bit words
Count
Data Present
Trig. FIFO 8 Deep
Reset
8 Bit C/O Counter
Reset
Offset Point
Enable Inputs
Serial Control
Trigger
Reset
Master Reset
Clock
Test Pulse Strobe
Decode Clock and Control
Clock and Control
Test Pulse Amp
Threshold
Power
Det. HV
Services